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*David Money Harris Associate Professor of Engineering at Harvey Mudd College in Claremont, CA, holds a Ph.D. from Stanford University and S.B. and M.Eng. degrees from MIT. His research interests include CMOS VLSI design, microprocessors, and computer arithmetic. He holds a dozen patents, is the author of three other books in the field of digital design and three hiking guidebooks, and has ...*

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*Weste, Neil is the author of 'CMOS VLSI Design: A Circuits and Systems Perspective (4th Edition)', published 2010 under ISBN 9780321547743 and ISBN 0321547748. Marketplace prices. Summary. Used. 5 from \$99.46. Rentals. 4 from \$61.20. Alternate. 9 from \$26.54. All. 18 from \$26.54 ...*

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size and a unit width for NMOS, it usually offers  
a total of 3C input capacitance to the driving  
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[Dynamic logic \(digital electronics\) - Wikipedia](#)

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